Altera DE3 Development System

1. Overview



The Altera DE3 ASIC prototyping development board allows researchers and engineers to access the high density and high performance FPGA - the Altera Stratix III. Starting with the Stratix III 3SL150 (142K logic elements), the DE3 is the perfect platform for creating your cutting edge design in programmable logic. The DE3-150 board can be stacked up to create a programmable fabric that is unequaled by any other board system in the world.

DE3 boards include the DE3-340 (338K logic elements) and the DE3-260 (254.4K logic elements) that are optimized with extra on-chip multipliers needed for DSP research and development. All of the DE3s can be stacked and all share the same set of function from the Terasic's daughter cards connected to them.

Multiple DE3s can be joined together to increase design gate count and performance.

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Up to 10 DE3s can be stacked vertically where space is at a premium.



DE3s can also be arranged in a expanded flat pattern to accommodate additional daughter cards.

2. Specification

FPGA Devices

- Stratix III EP3SL150 (DE3-150)
 - o 142,000 logic elements (LEs)
 - o 5,499K total memory Kbits
 - o 384 18x18-bit multipliers blocks
 - o 736 user I/Os
- Stratix III EP3SE260 (DE3-260)
 - o 254,400 logic elements (LEs)
 - o 14,688K total memory Kbits
 - o 768 18x18-bit multipliers blocks
 - o 736 user I/Os
- Stratix III EP3SL340 (DE3-340)
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- Add: 9F., No.176, Sec.2, Gongdao 5th Rd, East Dist, Hsinchu City, 30070. Taiwa

- o 338,000 logic elements (LEs)
- o 16,272K total memory Kbits
- o 576 18x18-bit multipliers blocks
- o 736 user I/Os

The DE3 board has powerful features that allow the user to implement resource and time consuming designed circuits, specifically for high-speed application and projects with complex algorithms.

- Built-in USB Blaster for programming and user API control
- JTAG programming mode
- DDR2 SO-DIMM socket
- 4 push-button switches
- 1 DIP switch (x8)
- 4 slide switches
- 8 RGB LEDs
- 2 seven-segment displays
- USB Host/Slave Controller with one mini-AB for host/device and two type A for device
- SD Card socket
- 50MHz onboard oscillator for clock source
- 1 SMA connector for external clock input
- 1 SMA connector for PLL clock output
- Eight 180-pin High Speed Terasic Connectors (HSTC), where 4 male and 4 female connectors are on the top and bottom of DE3, respectively.
- Two 40-pin Expansion Headers

High speed I/O performance

High Speed Samtec Cable: REF-136223-02 (QSH-090-01-F-D-A-K)

High speed I/O performance: The live test shows that the highest speed I/O transmission reaches the ultimate 1.25 Gbps (the top limitation of Stratix III).

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MTL COMM

• Connect MTL with DE3



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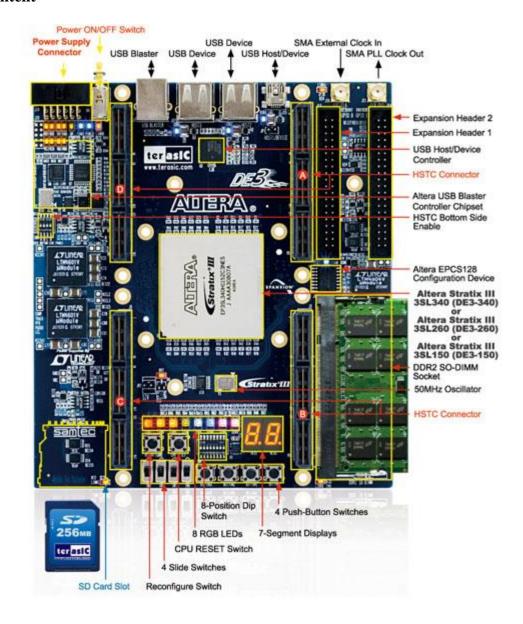


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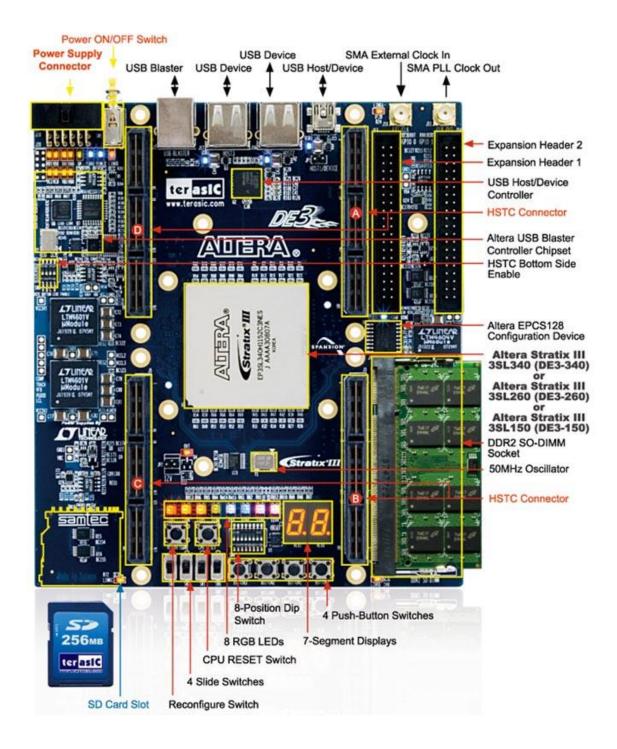
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3. Kit content



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4. Layout



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